

UNITED STATES PATENT AND TRADEMARK OFFICE  
**CERTIFICATE OF CORRECTION**

PATENT NO. : 6,963,239 B2  
APPLICATION NO. : 10/629342  
DATED : November 8, 2005  
INVENTOR(S) : Sébastien Laville et al.

Page 1 of 2

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 2, line 40	Replace "means of for" With --means for--
Column 2, line 43	Replace "the gate grid" With --the gate--
Column 5, line 22	Replace "to a gate grid G" With --to a gate G--
Column 8, line 61 Claim 1	Replace "of the said parameter;" With--of the parameter;--
Column 9, line 43, Claim 6	Replace "resistances," With --resistances;--
Column 9, line 47, Claim 6	Replace "activation," With --activation;--
Column 9, line 59, Claim 7	Replace "parameter:" With --parameter;--

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Column 9, line 67,  
Claim 7

Replace "activation,"  
With --activation;--

Column 10, line 66,  
Claim 13


Replace "to which the fusible element"  
With --to which fusible element is--

Column 12, line 3,  
Claim 16

Replace "certin"  
With --certain--

Signed and Sealed this

Eleventh Day of July, 2006

A handwritten signature in black ink, appearing to read "Jon W. Dudas". The signature is stylized with a large, looped initial "J" and a distinct "D" at the end.

JON W. DUDAS  
*Director of the United States Patent and Trademark Office*